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# CALL FOR PAPERS

## UCAS-4

### WORKSHOP on UNIQUE CHIPS and SYSTEMS

*To be held in conjunction with IEEE International Symposium on  
Performance Analysis of Systems and Software (ISPASS08)*

**April 20, 2008**

**Austin, Texas, USA**

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#### **Organizers:**

Eugene John, Univ. of Texas at San Antonio  
Juan Rubio, ARL IBM

#### **Program Committee:**

Lee D. Coraor, Pennsylvania State University  
Manoj Franklin, University of Maryland  
Antonio Gentile, Università di Palermo, Italy  
Peter Hofstee, IBM  
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Vijay Nair, Intel  
Juan Rubio, ARL IBM  
Zili Shao, The Hong Kong Polytechnic University  
Laurence T. Yang, St. Francis Xavier University, Canada

#### **Important Dates:**

**Paper Submission:** February 29, 2008  
**Notification of Acceptance:** March 17, 2008  
**Final Version Due:** March 31, 2008

This workshop looks forward to bring together unique research in all areas of chip and system design. We are looking forward to assembling a program with unique technologies, circuits and architectures that your academic or industry team is working on.

Topics of interest include but not limited to unique aspects of:

- VLSI Systems
- Nanoelectronics and Gigascale Systems
- Software Systems
- Grid computing
- Pervasive Computing
- Radiation hardened systems
- Low Power Architectures and Systems
- Low Power Software
- Bioinformatics
- Integrated Security Systems
- Visualization
- Biometrics
- Biomedical
- Data hiding
- Blind Signal and Image Processing
- Wireless ad hoc and sensor networks
- Massively parallel architectures
- Multithreaded architectures
- Fine-grain parallel architectures
- Unique technologies (quantum well, optoelectronic, biologically inspired circuits/systems, RF and high frequency, etc)

Prospective authors are invited to submit a full paper not exceeding 8 pages in length in standard IEEE two-column format

**For more information, visit the UCAS website at:**

*<http://www.ispass.org/ucas4/>*

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