CALL FOR PAPERS

UCAS-1

WORKSHOP on UNIQUE CHIPS and SYSTEMS

To be held in conjunction with IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS05)

March 20, 2005
Hotel Renaissance
Austin, Texas

Organizers:

Eugene John, University of Texas at San Antonio
Juan Rubio, IBM Austin Research Lab

Program Committee:

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C J Paul, IBM
Juan Rubio, ARL, IBM
Earl Swartzlander, University of Texas at Austin
Deepu Talla, Texas Instruments

Important Dates:

Paper Submission: **January 20, 2005**
Notification of Acceptance: **February 20, 2005**
Final Version Due: **March 8, 2005**

This workshop looks forward to bring together unique research in all areas of chip and system design. We are looking forward to assembling a program with unique technologies, circuits and architectures that your academic or industry team is working on.

Topics of interest include but not limited to unique aspects of:

- Massively parallel architectures
- Multithreaded architectures
- Grid computing
- Pervasive Computing
- Radiation hardened systems
- Fine-grain parallel architectures
- VLSI Systems
- Low Power Architectures and Systems
- Low Power Software
- Nanoelectronics and Gigascale Systems
- Special Function Systems
- Bio Informatics
- Integrated Security Systems
- Visualization
- Biometrics
- Biomedical
- Data hiding
- Blind Signal and Image Processing
- Wireless ad hoc and sensor networks
- Unique technologies (quantum well, optoelectronic, biologically inspired circuits/systems, RF and high frequency, etc)

Prospective authors are invited to submit papers in one of the following two formats:

1. A full paper not exceeding 8 pages in length in standard IEEE two-column format.
2. A two page extended abstract with a set of viewgraphs.

Submission of papers via http://www.ispass.org/ucas/submit.html

For more information, visit the UCAS website at:

http://www.ispass.org/ucas/