



April 26-28, 2009  
Boston, MA

# CALL FOR PAPERS

## International Symposium on Performance Analysis of Systems and Software – ISPASS-2009

### GENERAL CHAIRS

Dean Tullsen, *UC San Diego*  
Todd Austin, *University of Michigan*

### PROGRAM CHAIR

Lieven Eeckhout, *Ghent University*

### PROGRAM COMMITTEE

Tor Aamodt, *University of British Columbia*  
Steve Blackburn, *Australian National University*  
Pradip Bose, *IBM Research*  
David Brooks, *Harvard University*  
Derek Chiou, *University of Texas at Austin*  
Bronis R. de Supinski, *LLNL*  
Sudhanva Gurumurthi, *University of Virginia*  
Greg Hamerly, *Baylor University*  
Ravishankar Iyer, *Intel*  
Aamer Jaleel, *Intel*  
Stefanos Kaxiras, *University of Patras*  
Rakesh Kumar, *UIUC*  
Benjamin Lee, *Microsoft Research*  
Charles Lefurgy, *IBM*  
Mikko Lipasti, *University of Wisconsin—Madison*  
Daniel Ortega, *HP Labs*  
Steve Reinhardt, *AMD*  
Scott Rixner, *Rice University*  
Eric Rotenberg, *NCSU*  
André Seznec, *IRISA*  
Tim Sherwood, *UC Santa Barbara*  
Allen Snively, *UC San Diego*  
Thomas Wenisch, *University of Michigan*

### LOCAL ARRANGEMENTS CHAIR

David Brooks, *Harvard University*

### PUBLICITY CHAIR

Elmoustapha Ould-Ahmed-Vall, *Intel*

### FINANCE CHAIR

Nadeem Malik, *IBM*

### WEB CHAIR

Byeong Kil Lee, *TI*

### PUBLICATIONS CHAIR

Rakesh Kumar, *UIUC*

### WORKSHOPS/TUTORIALS CHAIR

Jun Yang, *University of Pittsburgh*

### REGISTRATION CHAIR

Rajeev Balasubramonian, *University of Utah*

### SUBMISSION AND REVIEW WEB CHAIR

Michiel Ronsse, *Ghent University*



Sponsored by the IEEE  
Computer Society's TCI, TCCA,  
and TC-uARCH



The IEEE International Symposium on Performance Analysis of Systems and Software provides a forum for sharing advanced academic and industrial research work focused on performance analysis in the design of computer systems and software. Authors are invited to submit previously unpublished work for possible presentation at the conference. Papers are solicited in fields that include the following:

- Performance evaluation methodologies
  - Analytical modeling
  - Statistical approaches
  - Tracing and profiling tools
  - Simulation techniques
  - Hardware (e.g., FPGA) accelerated simulation
  - Hardware performance counter architectures
- Performance analysis
  - Performance metrics
  - Bottleneck identification and analysis
  - Visualization
- Performance analysis of commercial and experimental hardware
  - General-purpose microprocessors
  - Multi-threaded, multi-core and many-core architectures
  - Accelerators and graphics processing units
  - Embedded and mobile systems
  - Enterprise systems and data centers
  - Supercomputers
  - Computer networks
- Performance analysis of emerging workloads and software
  - Software written in managed languages
  - Virtualization and consolidation workloads
  - Internet-sector workloads
  - Embedded, multimedia, games, telepresence
  - Bioinformatics, life sciences, security, biometrics
- Application and system code tuning and optimization
- Confirmations or refutations of important prior results

Next to **research papers**, we also welcome **tool papers** in order to reward tool-building effort and publicize new tools to the community. Tool papers will be judged more so on their potentially wide impact and use than on their research contribution. Tools in any of the above fields of interest are eligible.

See <http://www.ispass.org> for submission details.

### IMPORTANT DATES

Abstract due: Oct 3, 2008  
Full submission: Oct 10, 2008 – **NO EXTENSION**  
Rebuttal: Dec 4-5, 2008  
Notification: Dec 17, 2008  
Final paper due: Feb 25, 2009